

GIUSEPPE SERAZZI

Curriculum Vitae
January 10th, 2014

Education

Laurea Degree (Master) in Mathematics, University of Pavia, Italy, 1969

Teaching activities

- Politecnico di Milano, Dept. of Comp. Science, Milan, professor, 1991- present (retired from Nov. 2013)
- Università Statale di Milano, Department of Computer Science, professor, 1987 - 1991
- Università di Pavia, Department of Mathematics, Associate professor, 1978 - 1987
- Università della Svizzera Italiana, Faculty of Communication Science, Lugano, Switzerland, professor of "Management Information Systems", 1997 - 2010
- Università della Svizzera Italiana, School on "Entrepreneurship for high-tech start-ups", Lugano, Switzerland, co-director with prof.M.Malek, Humboldt Universitat, Berlin, Oct.2003, Sept.2004

Scientific activities

From 1978 until 1985 he was *researcher* at the "Numerical Analysis Institute" of the National Research Council, Pavia. On 8/10/1987 he became *Full professor* of Computer Science (ING/INF05).

His research interests include performance evaluation of enterprise networks (intranets, Web servers, Internet, storage servers, clouds), modelling techniques, workload characterization, simulative and analytic tools for capacity planning, and algorithms for bottlenecks detection in very large multiclass systems.

He is the author, or co-author, of *150+ publications*, 14 of these being *books* (amongst others "Measurement and Tuning of Computer Systems", Prentice Hall, 1983, and "E-business start-up" with P. Treleaven, McGraw-Hill, 2001).

His past and current research *activities* have been in *cooperation* with the following universities and research labs (see the joint *publications* with researchers of *all these institutions*):

- Computer Science Dept, University of California, Berkeley, USA (prof. D.Ferrari)
- Université René Descartes, Paris (prof. E.Gelenbe)
- Institut für Informationsverarbeitung, Technische Universität, Vienna (prof. G.Haring)
- Computer Science Dept, University of Maryland, College Park, USA (prof. S.Tripathi)
- Bellcore, Red Bank, USA (prof. D. Ghosal)
- Computer Science Dept, Vanderbilt University, Nashville, USA (prof. L.Dowdy)
- Computer Science Dept, University of Toronto, Toronto, Canada (prof. K.Sevcik)
- Computer Science Dept, Carleton University, Ottawa, Canada (prof. J.Rolia)
- IBM T.J.Watson Research Center, Yorktown Heights, USA (Dr. M.Squillante)
- Computer Science Dept, University of Illinois, Urbana, USA (prof. E.Smirni)
- Simon Business School, Univ. of Rochester, Rochester, USA (prof. P.Schweitzer)
- University College London, Dept. of Computer Science, London (prof. P.Treleaven)
- Computer Science Dept, University of California, Los Angeles, USA (prof. R.R.Muntz)
- IBM Zurich Research Lab., Zurich, Switzerland (Dr. L.Chen)
- Università di Milano (prof.E.Rosti), Pavia (proff.M.Calzarossa, V.Comincioli), Pisa (prof.M.Vanneschi), Torino (prof.G.Balbo).

He has held *Visiting Professor* positions in the Computer Science Department at University of California, Berkeley, EECS Dept. (1979) – at INRIA, Paris (1982) – at University of Maryland, College Park, USA (1987) – and Vanderbilt University, Nashville, USA (1990).

He *graduated* 7 PhD students, five of them are professors in academia (Imperial College London, Politecnico di Milano, Milan State University, Universitat Politècnica de Valencia) and one is working in a research lab of CNR. He tutored more than 200 MSc Thesis.

He *contributes* to major international journals and conferences on architecture of computer systems and networks, performance evaluation and high performance computing.

In 1993 he was *elected* a member of IFIP Working Group 7.3 “Computer Performance Modeling and Analysis”. From 1995 he has been an *honourable member* of the CMG Computer Measurement Group. He has been a member of the *editorial board* of the Performance Evaluation Journal (Elsevier, 1993--2000) and La Rivista di Informatica (AICA, 1989--2000).

He has been *Conference Chairman* of International Conferences, including: Int.I Conf. on High-Performance Computing and Networking, HPCN95 Europe, 1995 – 5th Int. I. Conf. on Modelling Techniques and Tools for Computer Performance Evaluation, 1991 – 1st Int.I Workshop on Workload Characterization of Computer Systems and Networks, 1985, and member of the *Programme Committee* for many others (organised by IEEE, IFIP, ACM and other associations) on architecture and performance evaluation of computer systems and networks and on High Performance Computing.

He has been the *founder* in 1980 and the coordinator for two years of the Special Interest Group of AICA “Performance Evaluation of Information Systems”.

He has been *responsible* for an operating unit in many European research projects (including “IMSE, the Integrated Modelling Support Environment” and the FP7 project “Worldwide Observatory of Malicious Behaviors and Attack Threats”) and Italian CNR and MURST projects (including the “Massively Parallel Computer Systems”). He was the national coordinator of the MIUR project 2001 “High Quality Web Systems”.

Professional Activities

From 1961 until 1978 he worked for *Olivetti SpA*. From 1961 until 1967 he was involved in the development of hardware equipment including the “reader of magnetic characters CMC7”, the “Auditronic 770”, and the “P101”, the *first prototype of the PC*, coordinated by Ing. Perotto, in the Olivetti Labs at S.Giuliano and Pregnana Milanese.

From 1969 until 1978 he worked at *Syntax SpA* (one of the first software companies operating in Italy, established by Olivetti). He was involved in the development of software projects including an assembler for the minicomputer E900 and the software for a real time system Vector 5000. In 1972 he started the *packages department*, and was, until 1978, responsible for the distribution of software packages developed by several American companies for IBM mainframes (performance evaluation, DBMS, program optimization). In 1976 he became an *Olivetti manager*.

In 2000 he has been the founder of the *Acceleratore d’Impresa*, the incubator of the Politecnico di Milano for high-tech start-ups, and he was the *director* until the end of 2006. 29 high-tech start-ups were created in this period. In the period 2004 - 2010 he was the Rector’s Delegate for the Acceleratore d’impresa.

Board Member:

2004 to 2008 - *Incubator Forum Gate 2 Growth Initiative*, a European Network of technology incubators linked to research institutes and universities, European Commission – DG Enterprise – Innovation/SMEs program

2004 to 2011 *PNI Cube*, the Italian association of the university incubators

2007 to 2011 *Foundation of the Politecnico di Milano*.

2007 to 2011 *Associazione di Idee e Imprese Innovative AI3*, association of Milan and Turin Polytechnics and public institutions to support incubators and innovative startups

He consults on contracts between the Dept. of Electronics and Computer Science of the Politecnico and major companies in the areas of architecture of computer systems, capacity planning and performance evaluation.

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